

**armlet quick
reference**

Our first arm1et program

```
    mov $0, 10
    mov $1, 1
    mov $2, 0
@loop:
    cmp $0, 0
    beq >done
    add $2, $2, $1
    add $1, $1, 1
    sub $0, $0, 1
    jmp >loop
@done:
    hlt
```

```
var t = 10
var i = 1
var s = 0
while (t != 0) {
    s = s + i
    i = i + 1
    t = t - 1
}
```

Instruction set

(Instructions controlling the data path)

```
nop          # no operation
mov $L, $A   # $L = $A (copy the value of $A to $L)
and $L, $A, $B # $L = bitwise AND of $A and $B
ior $L, $A, $B # $L = bitwise (inclusive) OR of $A and $B
eor $L, $A, $B # $L = bitwise exclusive-OR of $A and $B
not $L, $A    # $L = bitwise NOT of $A
add $L, $A, $B # $L = $A + $B
sub $L, $A, $B # $L = $A - $B
neg $L, $A    # $L = -$A
lsl $L, $A, $B # $L = $A shifted to the left by $B bits
lsr $L, $A, $B # $L = $A shifted to the right by $B bits
asr $L, $A, $B # $L = $A (arithmetically) shifted to the right by $B bits
```

```
mov $L, I    # $L = I (copy the immediate data I to $L)
add $L, $A, I # $L = $A + I
sub $L, $A, I # $L = $A - I
and $L, $A, I # $L = bitwise AND of $A and I
ior $L, $A, I # $L = bitwise (inclusive) OR of $A and I
eor $L, $A, I # $L = bitwise exclusive OR of $A and I
lsl $L, $A, I # $L = $A shifted to the left by I bits
lsr $L, $A, I # $L = $A shifted to the right by I bits
asr $L, $A, I # $L = $A (arithmetically) shifted to the right by I bits
```

```
loa $L, $A   # $L = [contents of memory word at address $A]
sto $L, $A   # [contents of memory word at address $L] = $A
```

Instruction set

(Instruction controlling the flow of execution)

<code>cmp \$A, \$B</code>	<code># compare \$A (left) and \$B (right)</code>	Comparison
<code>cmp \$A, I</code>	<code># compare \$A (left) and I (right)</code>	
<code>jmp \$A</code>	<code># jump to address \$A</code>	Jump and branch based on the results of the latest comparison
<code>beq \$A</code>	<code># ... if left == right (in the most recent comparison)</code>	
<code>bne \$A</code>	<code># ... if left != right</code>	
<code>bgt \$A</code>	<code># ... if left > right (signed)</code>	
<code>blt \$A</code>	<code># ... if left < right (signed)</code>	
<code>bge \$A</code>	<code># ... if left >= right (signed)</code>	
<code>ble \$A</code>	<code># ... if left <= right (signed)</code>	
<code>bab \$A</code>	<code># ... if left > right (unsigned)</code>	
<code>bbw \$A</code>	<code># ... if left < right (unsigned)</code>	
<code>bae \$A</code>	<code># ... if left >= right (unsigned)</code>	
<code>bbe \$A</code>	<code># ... if left <= right (unsigned)</code>	
<code>jmp I</code>	<code># jump to address I</code>	Jump and branch based on the results of the latest comparison
<code>beq I</code>	<code># ... if left == right (in the most recent comparison)</code>	
<code>bne I</code>	<code># ... if left != right</code>	
<code>bgt I</code>	<code># ... if left > right (signed)</code>	
<code>blt I</code>	<code># ... if left < right (signed)</code>	
<code>bge I</code>	<code># ... if left >= right (signed)</code>	
<code>ble I</code>	<code># ... if left <= right (signed)</code>	
<code>bab I</code>	<code># ... if left > right (unsigned)</code>	
<code>bbw I</code>	<code># ... if left < right (unsigned)</code>	
<code>bae I</code>	<code># ... if left >= right (unsigned)</code>	
<code>bbe I</code>	<code># ... if left <= right (unsigned)</code>	
<code>hlt</code>	<code># halt execution</code>	Halt
<code>trp</code>	<code># trap (break out of execution for debugging)</code>	